

PATENT NUMBER

05-0203

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| PD O.I.P.E. SCANNED <i>122</i> Q.A. <i>1C</i> | PATENT DATE |
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| APPLICATION NO. 09/669034 | CONT/PRIOR | CLASS 718 710 | SUBCLASS 260 | ART UNIT 2181 | EXAMINER KIMBLE |
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Ganesh Subramaniam
Gary Hammond

Method and apparatus for maintaining the temperature of a microprocessor using a firmware-assisted interrupt mechanism

PTO-2040
12/89[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) | | NOTICE OF ALLOWANCE MAILED | |
| | _____ (Date) | | | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ _____ (Primary Examiner) _____ (Date) | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Local Instruments Examiner) _____ (Date) | | ISSUE BATCH NUMBER | |

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